

embedded website online

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Hardware Overview

Christmann offers the RECS®|Box microserver architecture in [different chassis](#):

On every baseboard, different compute modules can be plugged, e.g. the newly developed hardware accelerated ARM module. The FPGA based accelerator can boost the energy efficiency of your application to a factor of over 120 compared to a classical CPU implementation and still [double energy efficiency compared to an optimized GPU implementation](#).

i A new high-speed interconnection between FPGAs has been integrated in the RECS®|Box which [reaches a speed of 40 Gb/s and 320 ns latency](#).

Free hardware test access

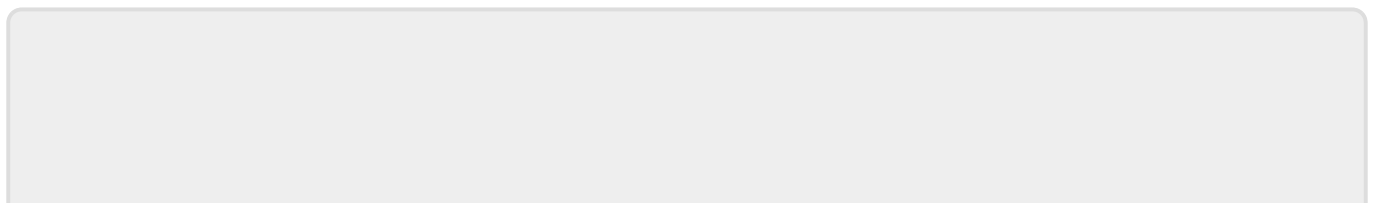
Interested in testing out the hardware? [Drop us a mail!](#)

[Hardware access](#) to heterogeneous and fully equipped hardware is possible through a collaboration with PSNC. PSNC (the Poznan Supercomputing and Networking Center) is successfully using the RECS®|Box for software analysis and optimisation for energy efficiency in various research projects, e.g. [FIPS](#) and [M2DC](#).

User Manual

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